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Grace Alicea

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

RECEIVED

Date: March 3, 2004

Robert Thomas BAILIS, et al.

MAR 09 2004

Confirmation No. 3353

Serial No: 10/016,772

Group Art Unit: 2819

Filed: December 10, 2001

Examiner: Chang, Daniel D.

For: METHOD AND SYSTEM FOR USE OF AN EMBEDDED FIELD
PROGRAMMABLE GATE ARRAY INTERCONNECT FOR FLEXIBLE
I/O CONNECTIVITY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AFTER FINAL

Sir:

In response to the Final Office Action dated January 6, 2004, please amend the above-identified application in the following manner:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 6 of this paper.